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Q: Kernel crashes before any protection or toggling of any gates I'm following this tutorial to get a simple EPROM-loader using FPGA prototyping board and Altera Cyclone IV EP4CE115F4C8. I can compile the code without any problem and got it to work on Altera Quartus II and Altera Quartus Prime Light Edition. My board's pinouts is shown below: I noticed that when I run the code on my board, it crashes around the time when I press reset button (or power button). My FPGA maps are as follows: I'm using Altera Quartus Prime Ultimate Edition for design verification. There are 4 EP4CE115F4C8 devices in my board. Since the power rail goes from 5V to 3.3V, I made my device input pin as 3.3V. I think the supply voltage is too high for my device pins. The problem only appears to be with the same EP4CE115F4C8 device on my board. I tried swapping it out with another device but it gets the same problem. What might be the problem? A: The problem is that you are triggering the EP4CE115F4C8 using the reset pin, which is directly connected to the CPU's global interrupt pin. This is not allowed. Instead, you should connect one of your internal interrupts directly to the reset pin, and poll that when you want to trigger the EP4CE115F4C8. The present invention relates to a technology for detecting defect items of a polarization optical system in an optical system of a large-sized display device (e.g., a projection display device). In a projection display device such as a projector, to realize a large-sized screen, the size of an image light modulator is usually enlarged. Therefore, a projection lens for enlarging an optical system is used. Further, in the image light modulator, minute transfer-ring



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